

ABSTRACT OF THE DISCLOSURE

An amplifier circuit comprises a differential amplifier configured by a differential pair of transistors, a common emitter amplifier connected in parallel to the differential amplifier and configured by a pair of common-emitter configuration transistors, input and output terminals which are common to the differential amplifier and the common emitter amplifier, and a bias controller connected to the differential amplifier and the common emitter amplifier and configured to control a bias of at least one of the differential amplifier and the common emitter amplifier.